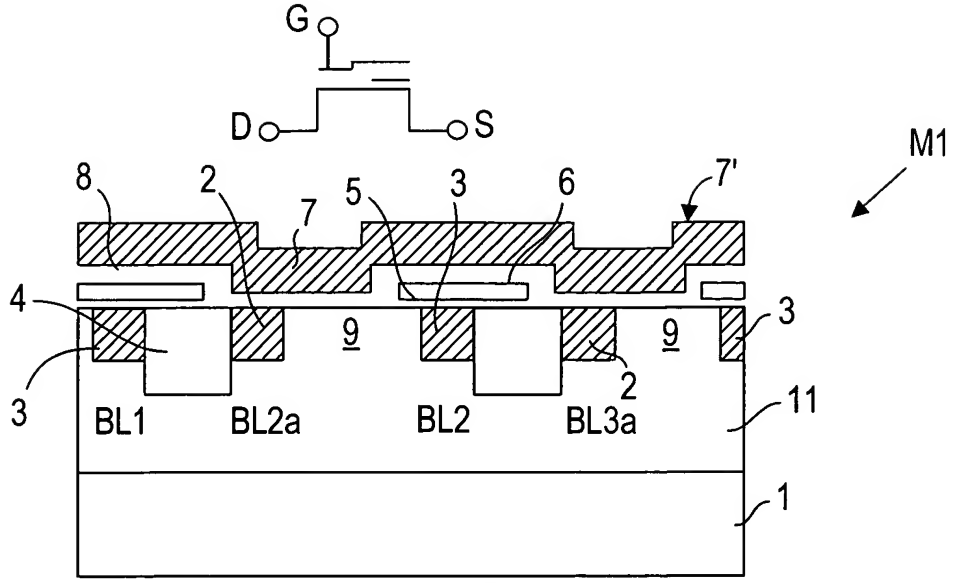


Fig. 1

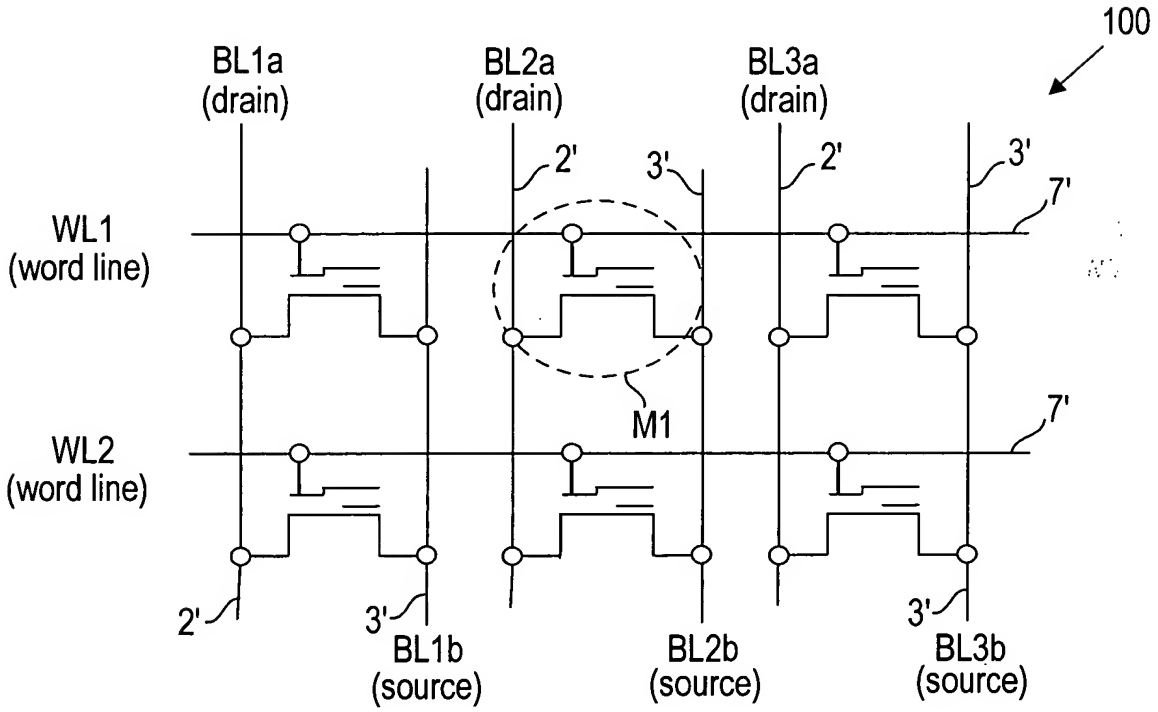


Fig. 2

Table 1

	BL1a	BL1b	BL2a	BL2b	BL3a	BL3b	WL1	WL2
Erase	F	F	F	F	F	F	-Vpp	0
Write	Vppx	F	0	F	Vppx	F	Vpp	<Vppx
Read	Vr	0	Vr	0	Vr	0	~Vcc	0

Note: Vppx < Vpp:2, Vr~1V

Fig. 2a

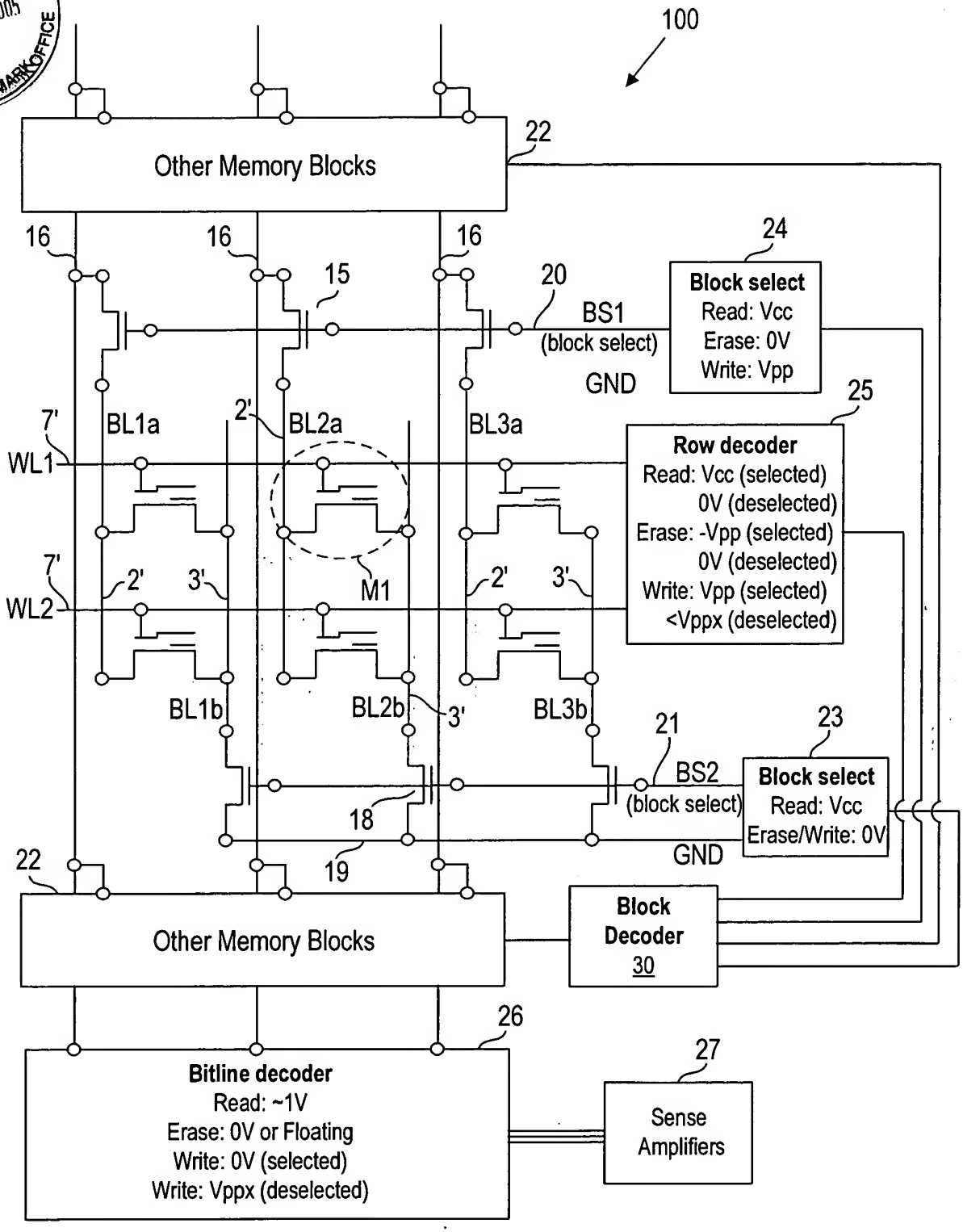


Fig. 3

Fig. 4a

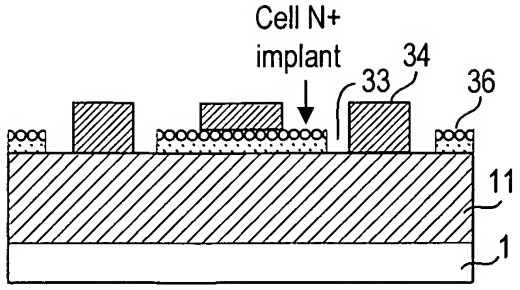


Fig. 4b

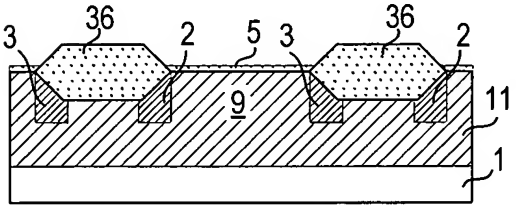


Fig. 4c

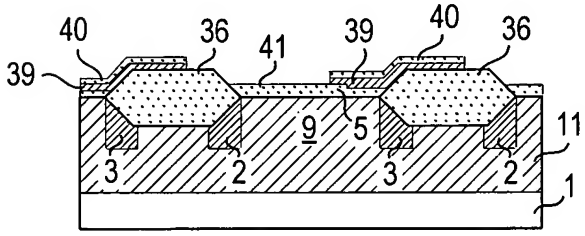


Fig. 4d

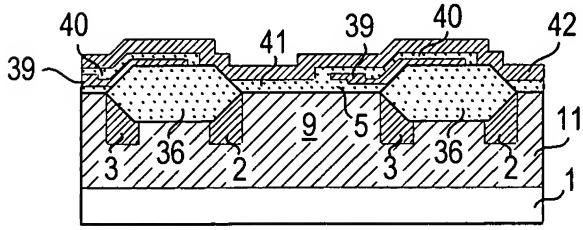


Fig. 4e

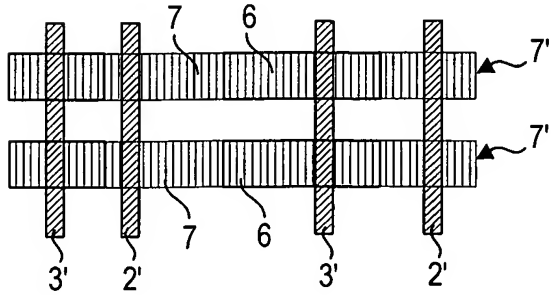


Fig. 5a

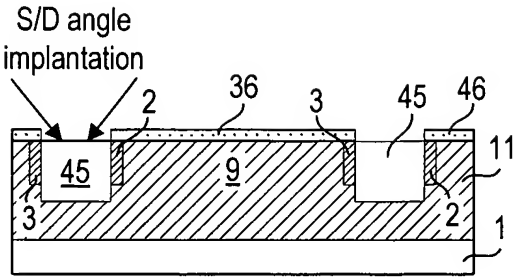


Fig. 5b

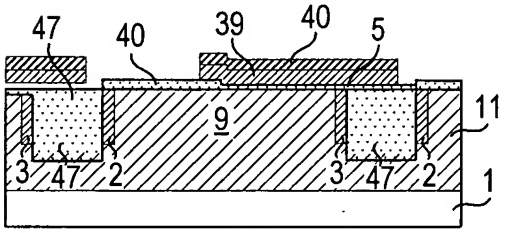


Fig. 5c

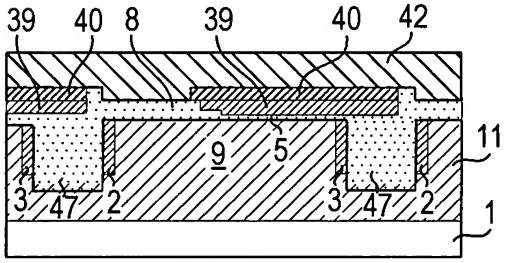
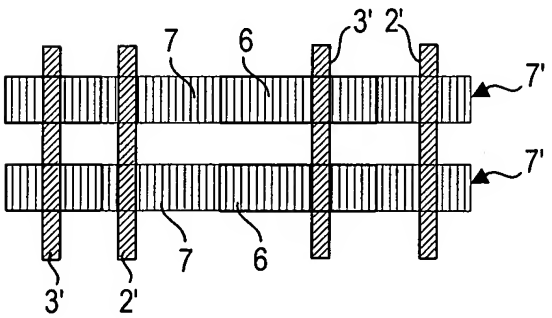


Fig. 5d



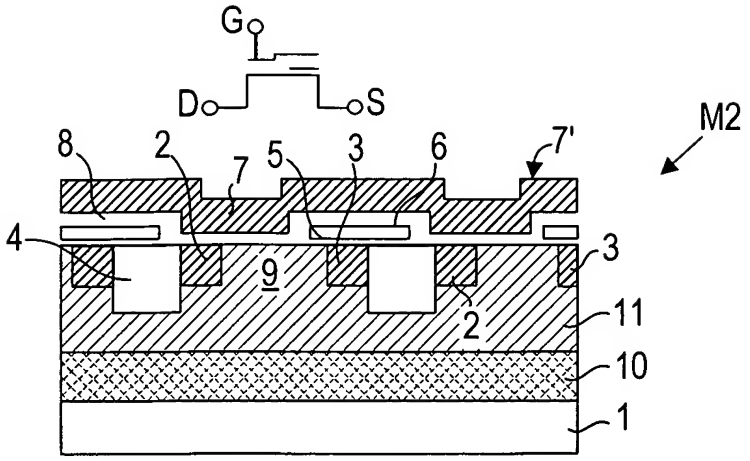


Fig. 6

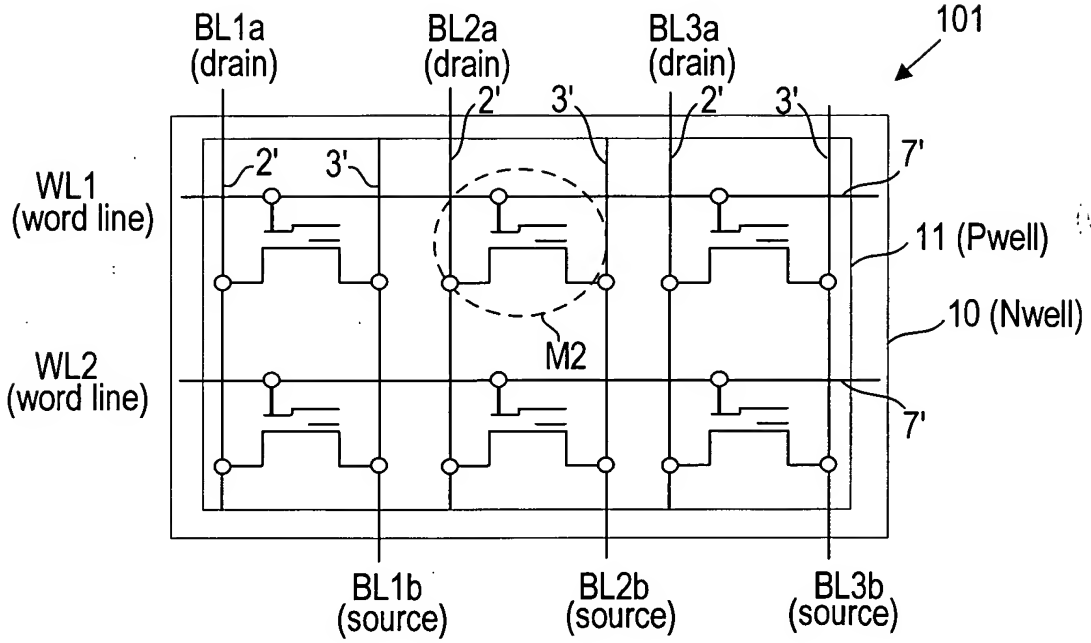


Fig. 7

Table 2

	BL1a	BL1b	BL2a	BL2b	BL3a	BL3b	WL1	WL2	Pwell	Nwell
Erase	F	F	F	F	F	F	0	Vpp	Vpp	Vpp
Write	Vppx	F	0	F	Vppx	F	Vpp	<Vppx	0	0
Read	Vr	0	Vr	0	Vr	0	~Vcc	0	0	0

Note: Vppx <= Vpp:2, Vr~1V

Fig. 7a

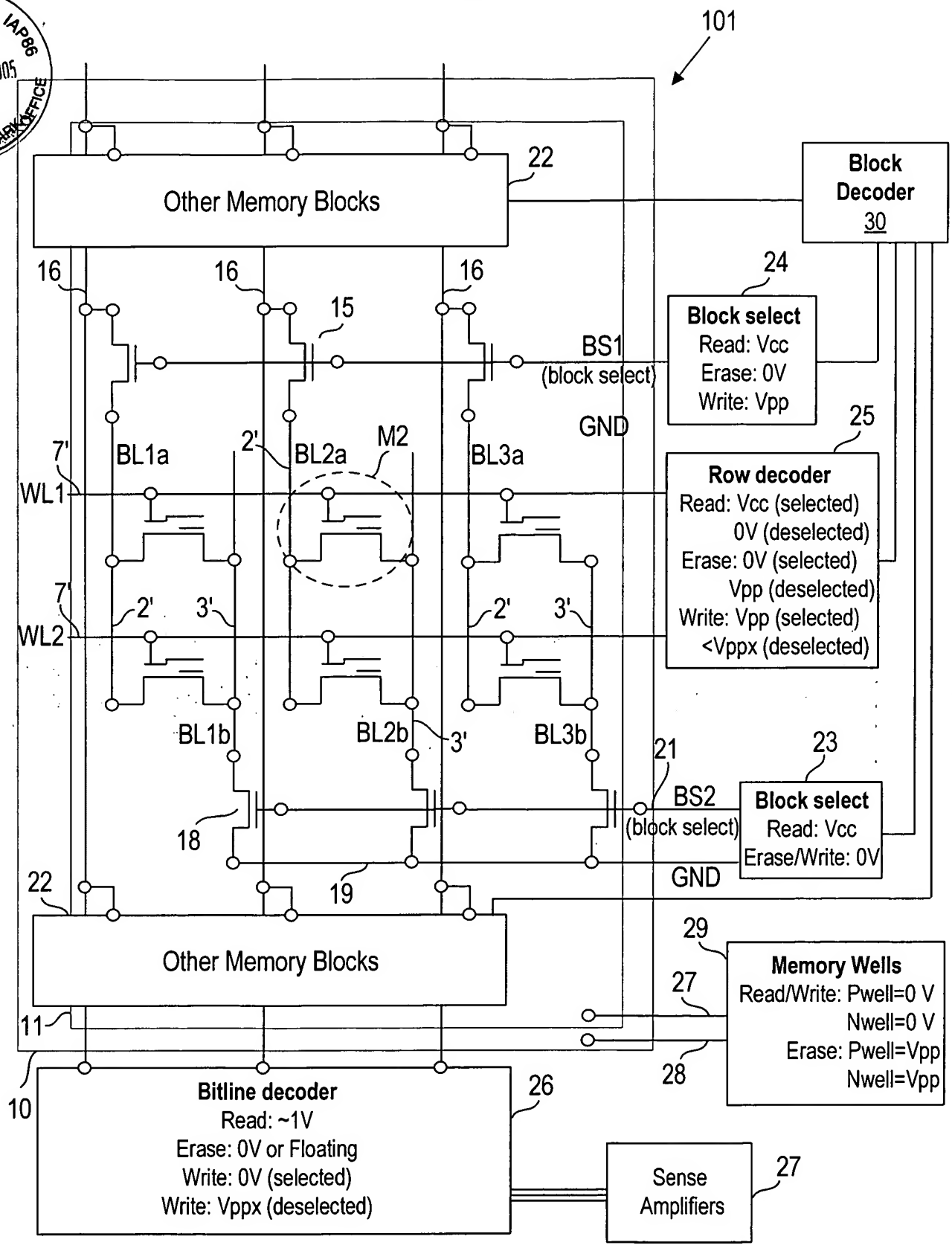


Fig. 8

Fig. 9a

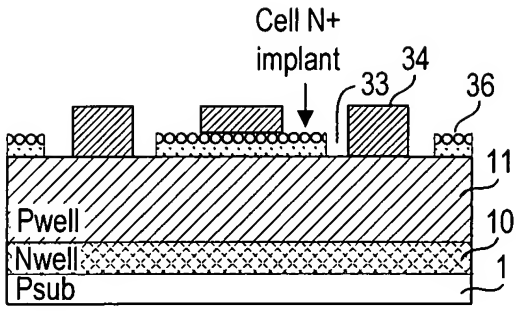


Fig. 9b

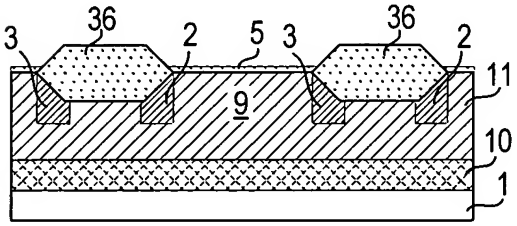


Fig. 9c

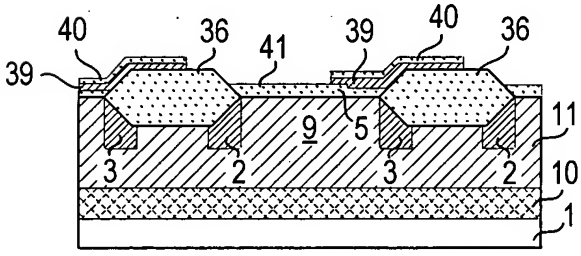


Fig. 9d

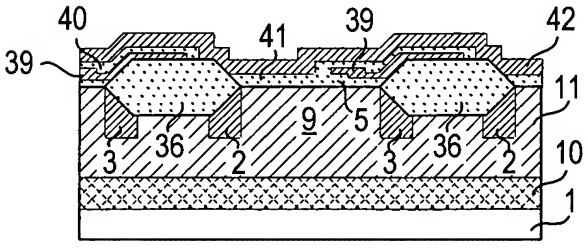


Fig. 9e

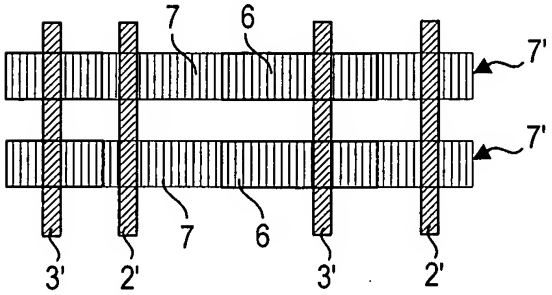


Fig. 10a

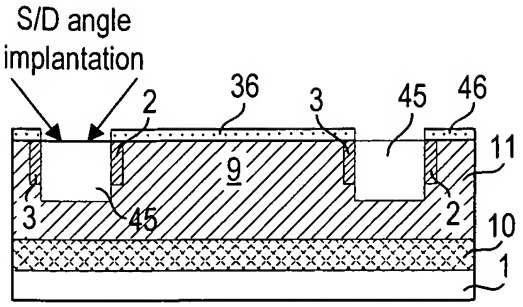


Fig. 10b

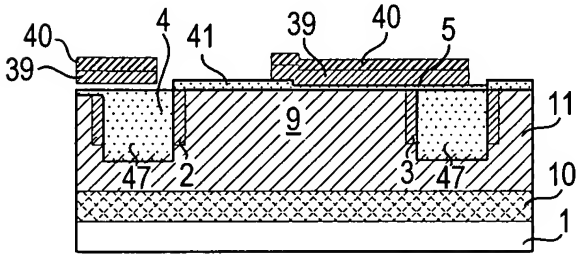


Fig. 10c

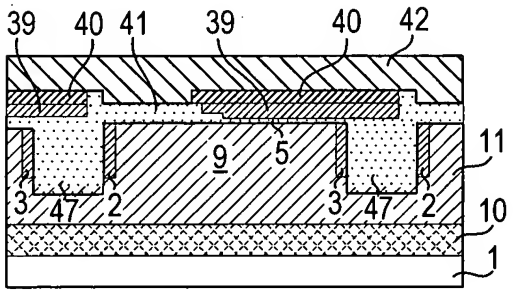


Fig. 10d